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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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PHOTO COUPLERS PS2561 -1,-2,-3,-4 PS2561L-1,-2,-3,-4

# HIGH ISOLATION VOLTAGE SINGLE TRANSISTOR TYPE MULTI PHOTO COUPLER SERIES

- NEPOC SERIES -

### **DESCRIPTION**

PS2561-1, -2, -3, -4 and PS2561L-1, -2, -3, -4 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon photo transistor.

PS2561-1, -2, -3, -4 are in a plastic DIP (Dual In-line Package) and PS2561L-1, -2, -3, -4 are lead bending type (Gull-wing) for surface mount.

#### **FEATURES**

- High isolation voltage (BV: 5 kV<sub>r.m.s.</sub> MIN.)
- High collector to emitter voltage (V<sub>CEO</sub>: 80 V MIN.)
- High current transfer ratio (CTR: 200 % TYP.)
- High speed switching  $(t_f = 3 \mu s, t_f = 10 \mu s TYP.)$
- Low cost
- Each isolated channels per package

#### **APPLICATIONS**

Interface circuit for various instrumentations, control equipments.

•	AC Line/Digital Logic	Isolate high voltage transients
	Digital Logic/Digital Logic	
•	Twisted pair line receiver	Eliminate ground loop pick-up
•	Telephone/Telegraph line receiver	Isolate high voltage transients
•	High Frequency Power Supply Feedback Control	Maintain floating ground
•	Relay Contact Monitor	Isolate floating grounds and transient
•	Power Supply Monitor	Isolate transients and ground systems

### SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (VDE0884)

Applied test class	. 55/150/21
Leakage Current (CTI)	. KB 100/A
Creepage distance	. > 7 mm
• Air clearance	. > 7 mm
• Isolation distance	. > 0.4 mm
Applied test voltage	. DC5 300 V or AC3 750 V
Reference voltage according to VDE0110b table4	. DC450 V or AC380 V
• Isolation group	. C

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## ABSOLUTE MAXIMUM RATINGS ( $T_a = 25$ °C)

		(PS2561-1) (PS2561L-1)	(PS2561-2, 3, 4) (PS2561L-2, 3, 4)	
Diode				
Reverse Voltage	$V_{R}$	6	6	<b>v</b>
Forward Current (DC)	1 <sub>F</sub>	80	80	mA
Power Dissipation	PD	150	120	mW/Channel
Peak Forward Current (PW = 100 $\mu$ s, Duty Cycle 1 %)	F (Peak)	i i <b>1</b> 1. Ay Garagai	1	Α
Transistor				
Collector to Emitter Voltage	V <sub>CEO</sub>	80	80	V
Emitter to Collector Voltage	V <sub>ECO</sub>	7	7	V
Collector Current	lc	80	80	mA
Power Dissipation	Pc	150	120	mW/Channel
Coupled				
Isolation Voltage *1)	BV	5000	5000	V <sub>r.m.s.</sub>
Storage Temperature	T <sub>stg</sub>	-55 to +150	-55 to +150	°C
Operating Temperature	Topt	-55 to +100	-55 to +100	°C
Lead Temperature (Soldering 10 s)	$T_{sol}$	260	260	°C
Total Power Dissipation	$P_T$	250	200	mW/Channel

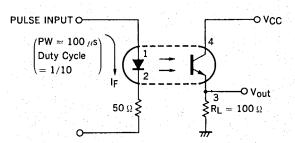
<sup>\*1)</sup> AC voltage for 1 minute at  $T_a = 25$  °C, RH = 60 % between input and output.

## ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

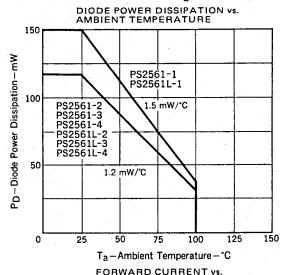
	CHARACTERISTIC		MIN. T	TYP. MAX.	UNIT	TEST CONDITIONS	
	Forward Voltage	VF		1,1	1.4	V	1 <sub>F</sub> = 10 mA
Diode	Reverse Current	I <sub>R</sub>			5	μΑ	V <sub>R</sub> = 5 V
	Junction Capacitance	C		50		pF	V = 0, f = 1.0 MHz
4	Collector to Emitter Dark Current	ICEO -			100	nA	V <sub>CE</sub> = 40 V, I <sub>F</sub> = 0
Transistor	Collector to Emitter Breakdown Voltage	BVCEO	40	60		٧	I <sub>C</sub> = 1 mA, I <sub>B</sub> = 0
	Emitter to Collector Breakdown Voltage	BVECO	7	9		· V	I <sub>E</sub> = 100 μA, I <sub>B</sub> = 0
	Current Transfer Ratio *2)	CTR	80	200	400	%	IF = 5 mA, VCE = 5 V
	Collector Saturation Voltage	VCE(sat)			0.3	٧	IF = 10 mA, I <sub>C</sub> = 2 mA
Countral	Isolation Resistance	R <sub>1-2</sub>	1011			Ω	V <sub>in-out</sub> = 1.0 kV
Coupled	Isolation Capacitance	C <sub>1-2</sub>	- 11 -	0.5		pF	V = 0, f = 1.0 MHz
	Rise Time *3)	tr		3		μs	$V_{CC} = 10 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$
	Fall Time *3)	tf		5		μs	$V_{CC} = 10 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$

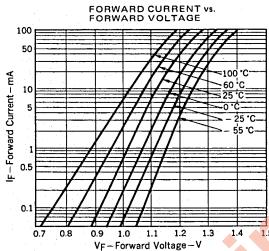
<sup>\*2)</sup> CTR rank (only PS2561-1, PS2561 L-1)

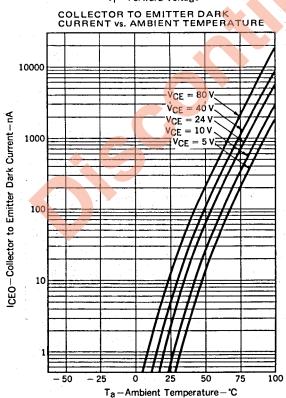
L: 200 to 400 (%) D: 100 to 300 (%) M: 80 to 240 (%) \*3) Test Circuit for Switching Time

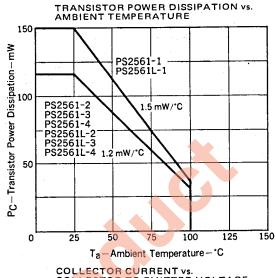


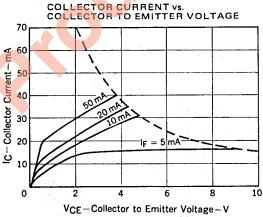
### TYPICAL CHARACTERISTICS ( $T_a = 25$ °C)

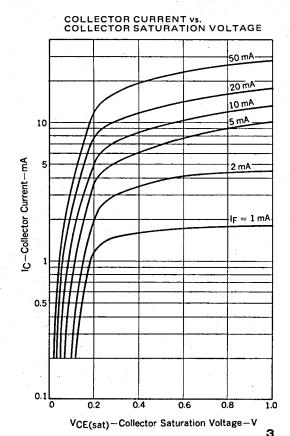


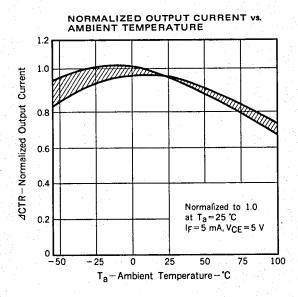


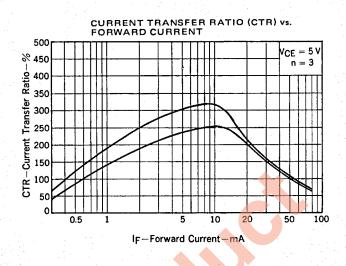


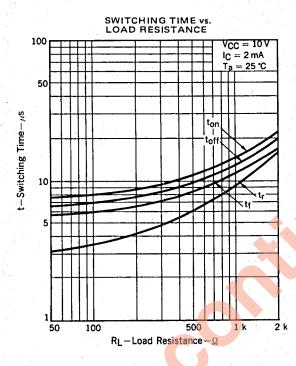


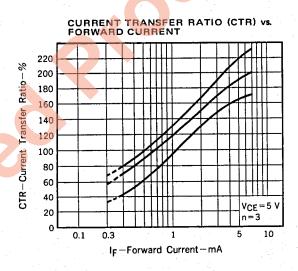


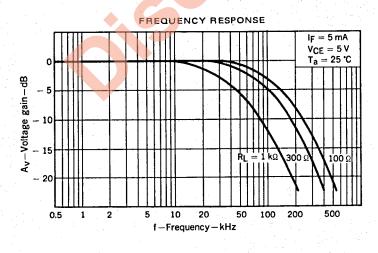


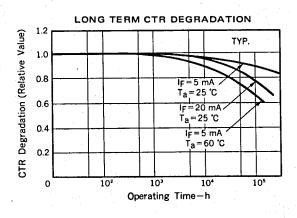












### **SOLDERING PRECAUTION**

(1) Infrared reflow soldering

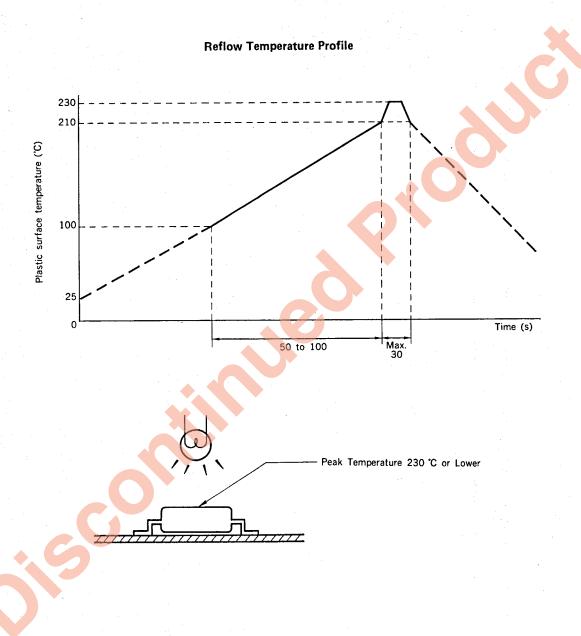
• Peak temperature : 230 °C or lower (plastic surface)

• Time : 30 s or less

(Time during plastic surface temperature overs 210 °C)

• No. of reflow times : 1

• Flux : Rosin-base flux



### (2) Dip soldering

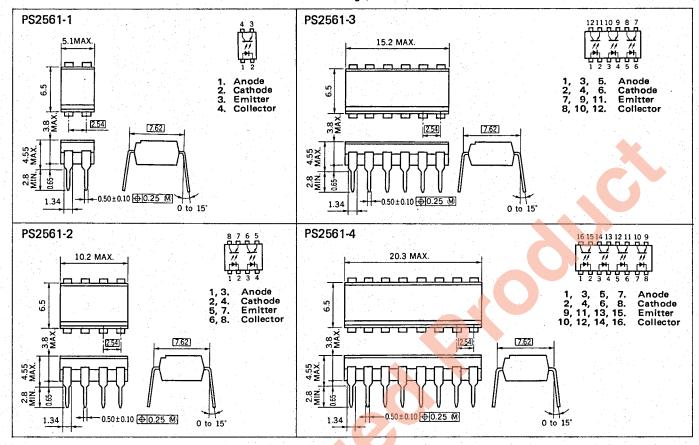
• Peak temperature : 260 °C or lower

Time : 10 s or lessFlux : Rosin-base flux

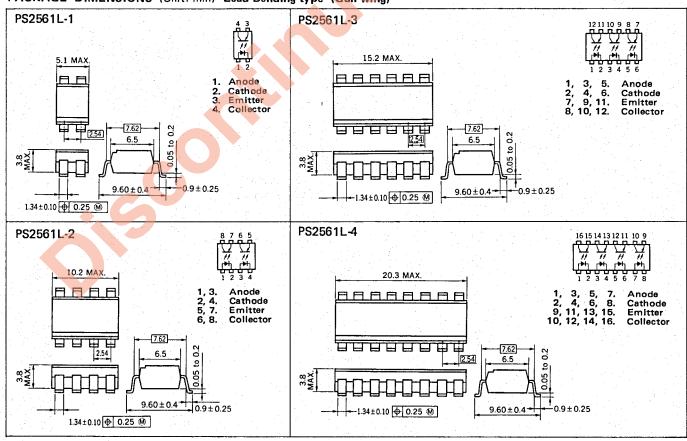
### PS2560 FAMILY

Feature Type Number	High Isolation Voltage	Function	High VCEO (MIN.)	High CTR (TYP.)	High Speed switching (TYP.)
PS2561-1, -2, -3, -4 PS2561L-1, -2, -3, -4	5 000 V <sub>r.m.s.</sub>	(Unidirectional)	80 V	200 %	$t_{f} = 3 \mu s$ , $t_{f} = 5 \mu s$
PS2562-1, -2, -3, -4 PS2562L-1, -2, -3, -4		input)	40 V	2 000 %	t <sub>r</sub> , t <sub>f</sub> = 100 μs
PS2565-1, -2, -3, -4 PS2565L-1, -2, -3, -4		AC input (Bidirectional input)	80 V	300 %	t <sub>r</sub> = 3 μs, t <sub>f</sub> = 5 μs
PS2566-1, -2, -3, -4 PS2566L-1, -2, -3, -4			40 V	2 000 %	t <sub>r</sub> , t <sub>f</sub> = 100 μs

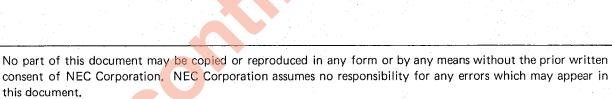
### PACKAGE DIMENSIONS (Unit: mm) DIP (Dual In-line Package)



### PACKAGE DIMENSIONS (Unit: mm) Lead Bending type (Gull-wing)



[MEMO]



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